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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Serge PONTAROLLO
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Art Unit: 2104

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The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Box Fee Amendment, Assistant Commissioner for Patents, Washington, D.C. 20231, on the 29th day of January, 1998.

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Assistant Commissioner For Patents
Washington, D.C. 20231

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GROUP 2100

Sir:

AMENDMENT

In response to the Office Action mailed September 29, 1997, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend claims 1, 7, 11, 15, 18, 23, and 26 as follows:

Please cancel claim 26.

(Once Amended) A device for protecti[on]ng a circuit against voltage surges, comprising:

a MOS transistor of a first type directly connected to first and second supply terminals by its source and its drain, respectively;

a MOS transistor of a second type directly connected between the second supply terminal and the gate of the transistor of the first type by its source and its drain, respectively;
and



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